

Project Name :

Platform : SKYLAKE

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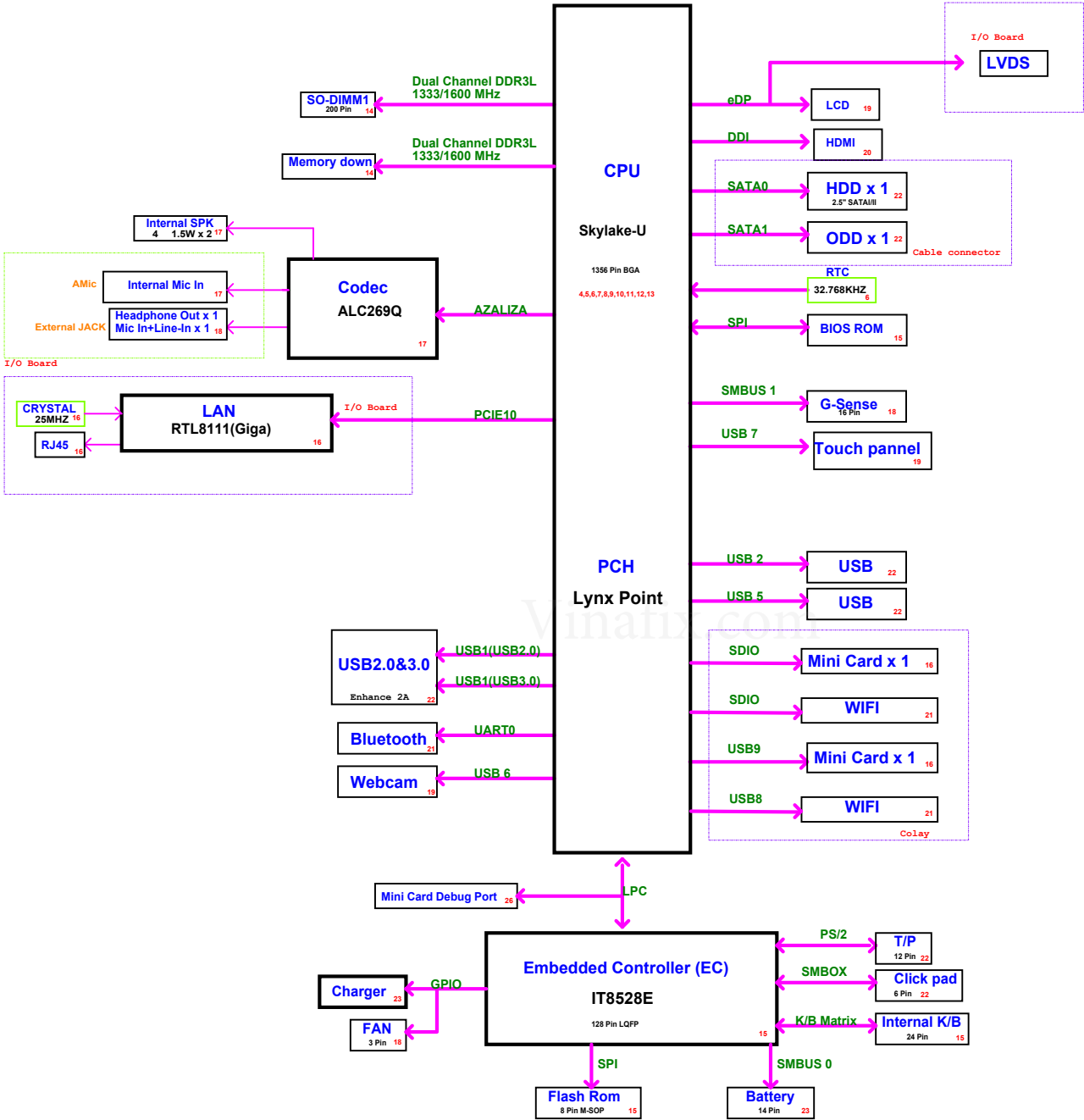
M/B Schematic Version Change List

Release Date	Version	PCB P/N	PCB Description	PCBA P/N	Note
2015-5-27	A				

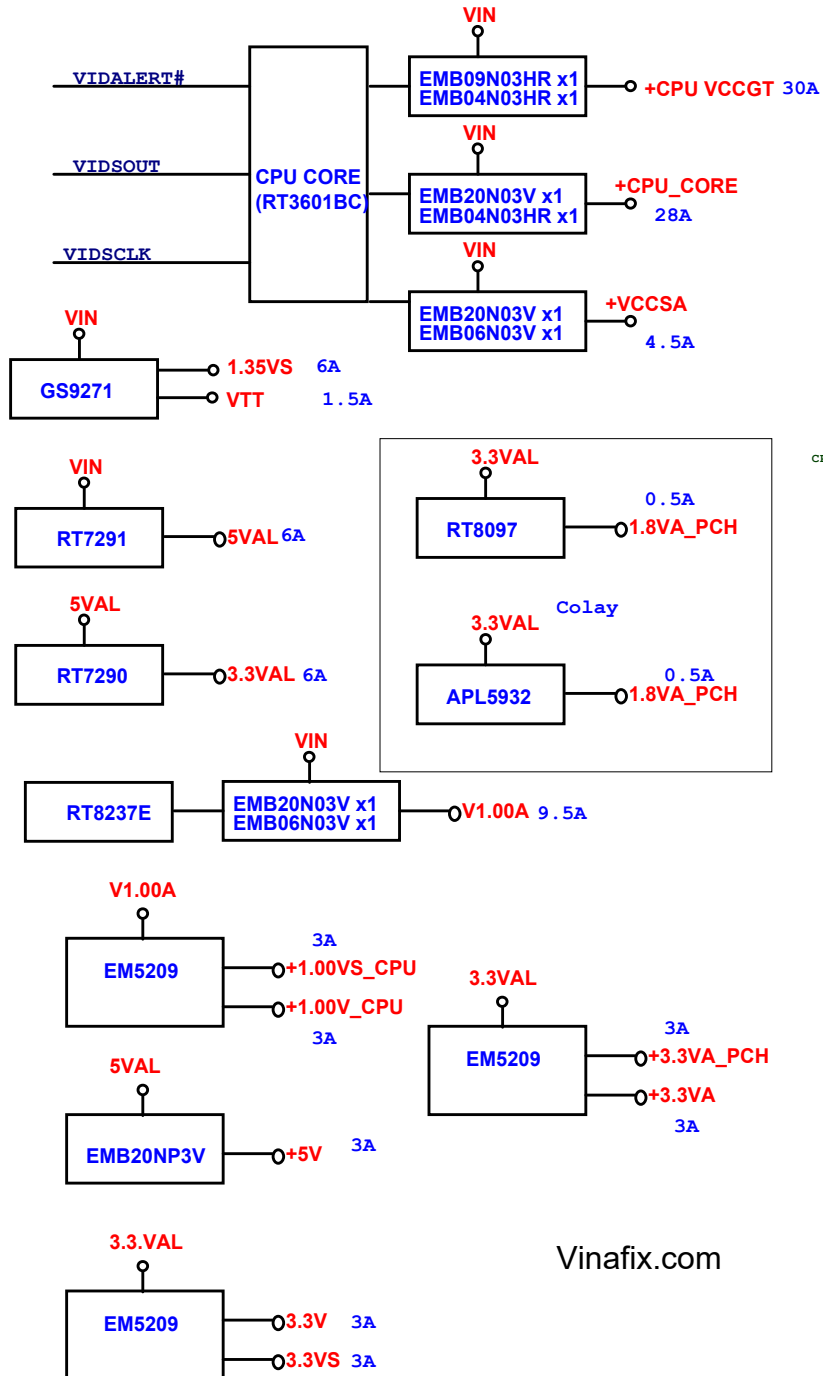
Daughter Board Schematic Version Change List

Release Date	Version	PCB P/N	PCB Description	PCBA P/N	Note

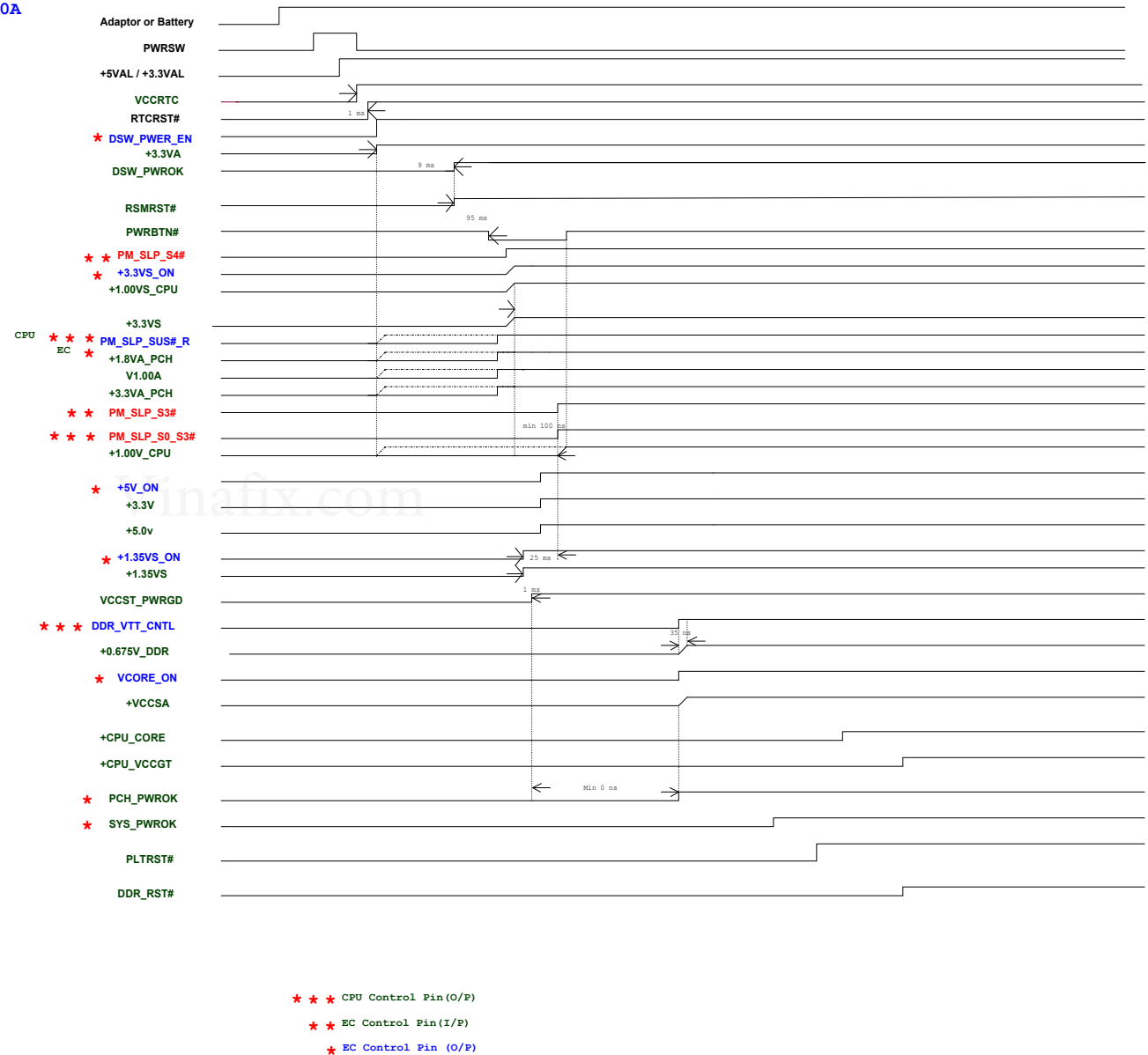
SYSTEM BLOCK DIAGRAM

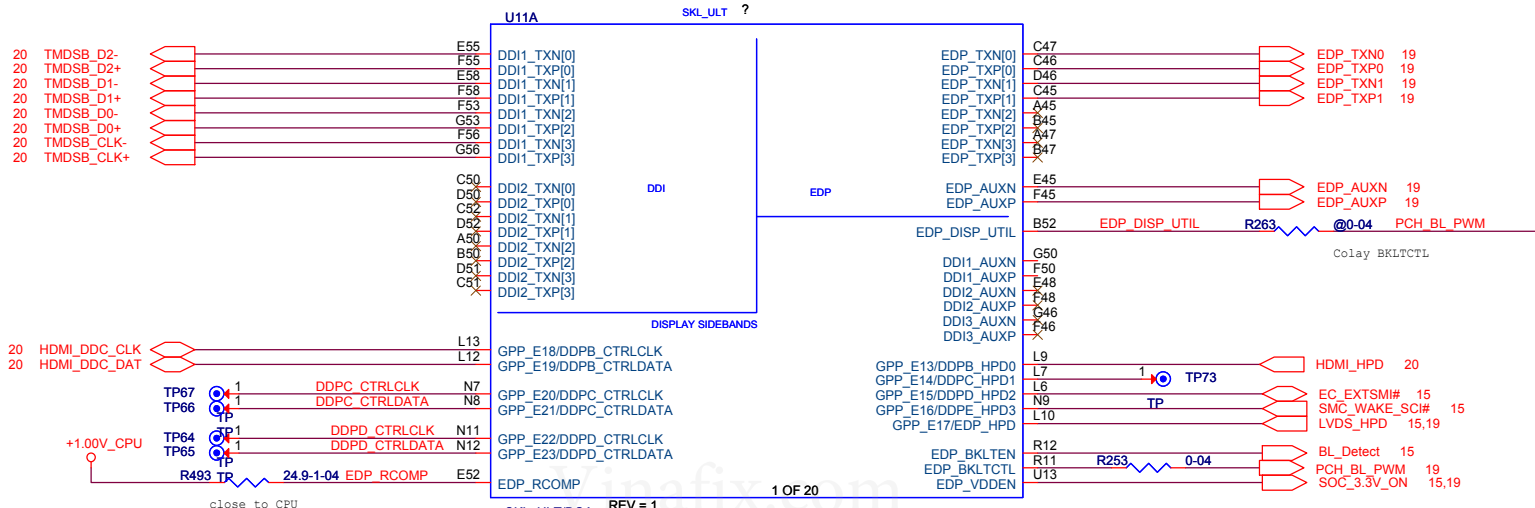


POWER BLOCK DIAGRAM



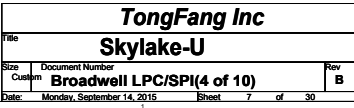
System Power On Sequence



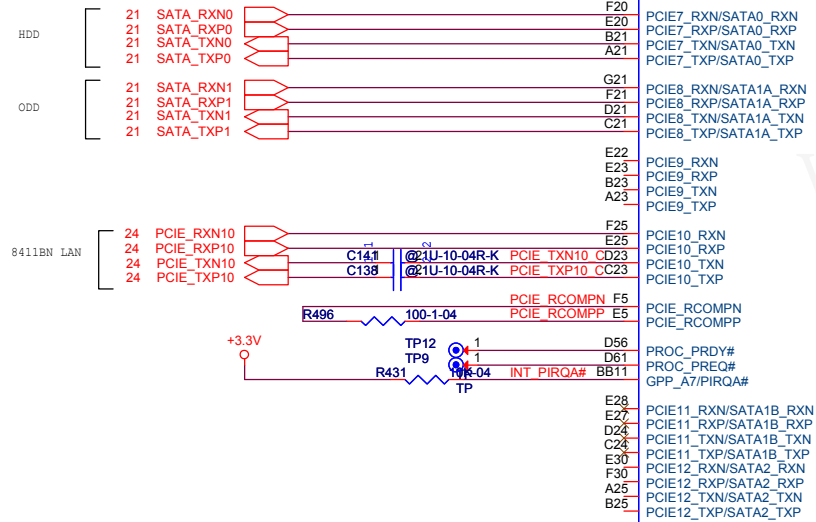




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Broadwell DDR bus(2 of 10)			
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SKL_ULT ?

U11H

PCIE/USB3/SATA

SSIC / USB3

USB3_1_RXN
USB3_1_RXP
USB3_1_TXN
USB3_1_TXP

USB3_2_RXN/SSIC_1_RXN
USB3_2_RXP/SSIC_1_RXP
USB3_2_TXN/SSIC_1_TXN
USB3_2_TXP/SSIC_1_TXP

USB3_3_RXN/SSIC_2_RXN
USB3_3_RXP/SSIC_2_RXP
USB3_3_TXN/SSIC_2_TXN
USB3_3_TXP/SSIC_2_TXP

USB3_4_RXN
USB3_4_RXP
USB3_4_TXN
USB3_4_TXP

USB2N_1
USB2P_1

USB2N_2
USB2P_2

USB2N_3
USB2P_3

USB2N_4
USB2P_4

USB2N_5
USB2P_5

USB2N_6
USB2P_6

USB2N_7
USB2P_7

USB2N_8
USB2P_8

USB2N_9
USB2P_9

USB2N_10
USB2P_10

USB2_COMP
USB2_ID

USB2_VBUSSENSE

GPP_E9/USB2_OC0#

GPP_E10/USB2_OC1#

GPP_E11/USB2_OC2#

GPP_E12/USB2_OC3#

GPP_E4/DEVSLP0

GPP_E5/DEVSLP1

GPP_E6/DEVSLP2

GPP_E0/SATAXP0/SATAGP0

GPP_E1/SATAXP1/SATAGP1

GPP_E2/SATAXP2/SATAGP2

GPP_E8/SATALED#

H8
G8
C13
D13

USB30_RX_N 22
USB30_RX_P 22
USB30_TX_N 22
USB30_TX_P 22

J6
H6
B13
A13

J10
H10
B15
A15

E10
F10
C15
D15

AB9
AB10

USB_PN1 22
USB_PP1 22

AD6
AD7

USB_PN2 21
USB_PP2 21

AH3
AJ3

USB_PN3 16
USB_PP3 16

AD9
AD10

USB_PN4 21
USB_PP4 21

AJ1
AJ2

USB_PN5 22
USB_PP5 22

AF6
AF7

USB_PN6 19
USB_PP6 19

AH1
AH2

USB_PN7 19
USB_PP7 19

AF8
AF9

USB_PN8 22
USB_PP8 22

AG1 USB_PN9 1
AG2 USB_PP9 1

TP81 TP
TP82 TP

AH7
AH8

L1<450mil
L1

AB6/USB2_COMP R482

AG3
AG4

A9
C9
D9
B9

R124 10K-04
R123 10K-04
R121 10K-04
R126 10K-04

J1
J2
J3

TP23 TP
TP26 TP

H2
H3
G4

SATA_DET#
SATA_ODD_PRSNR_N
MSATA_MPCIE_DET_N

R144 10K-04
R154 10K-04
R140 10K-04

H1

SHDDLED# 22

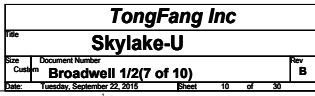
Check 厂 / 跨high or low

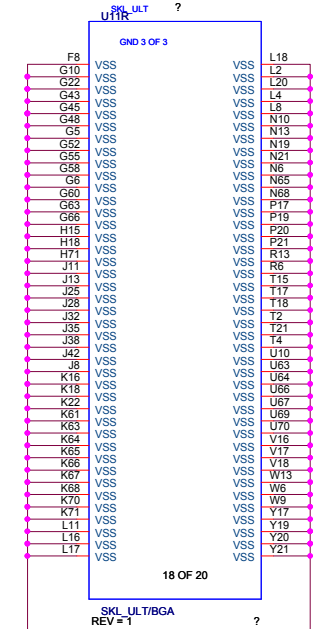
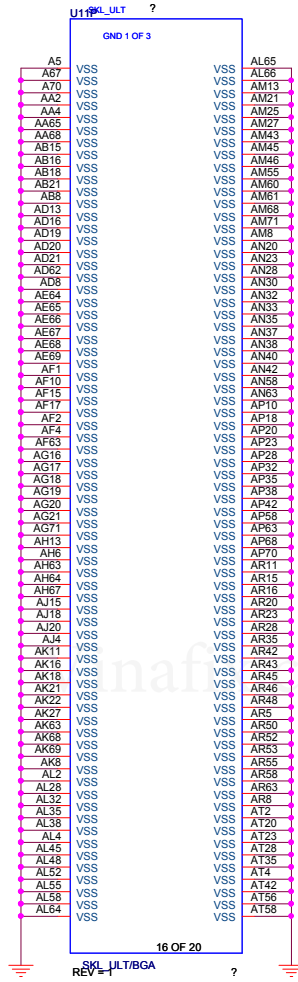
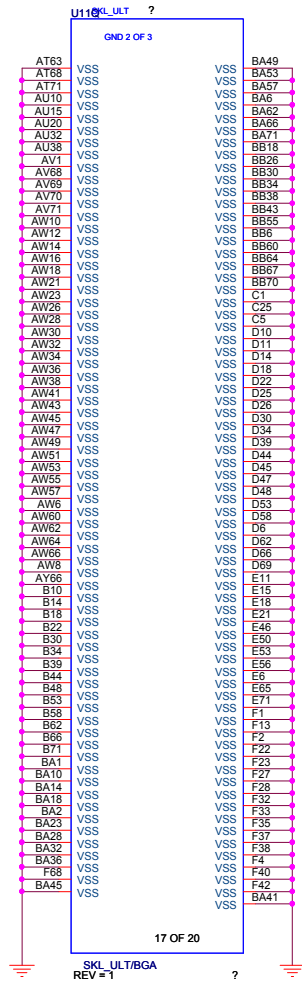
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SKL_ULT/BGA
REV = 1

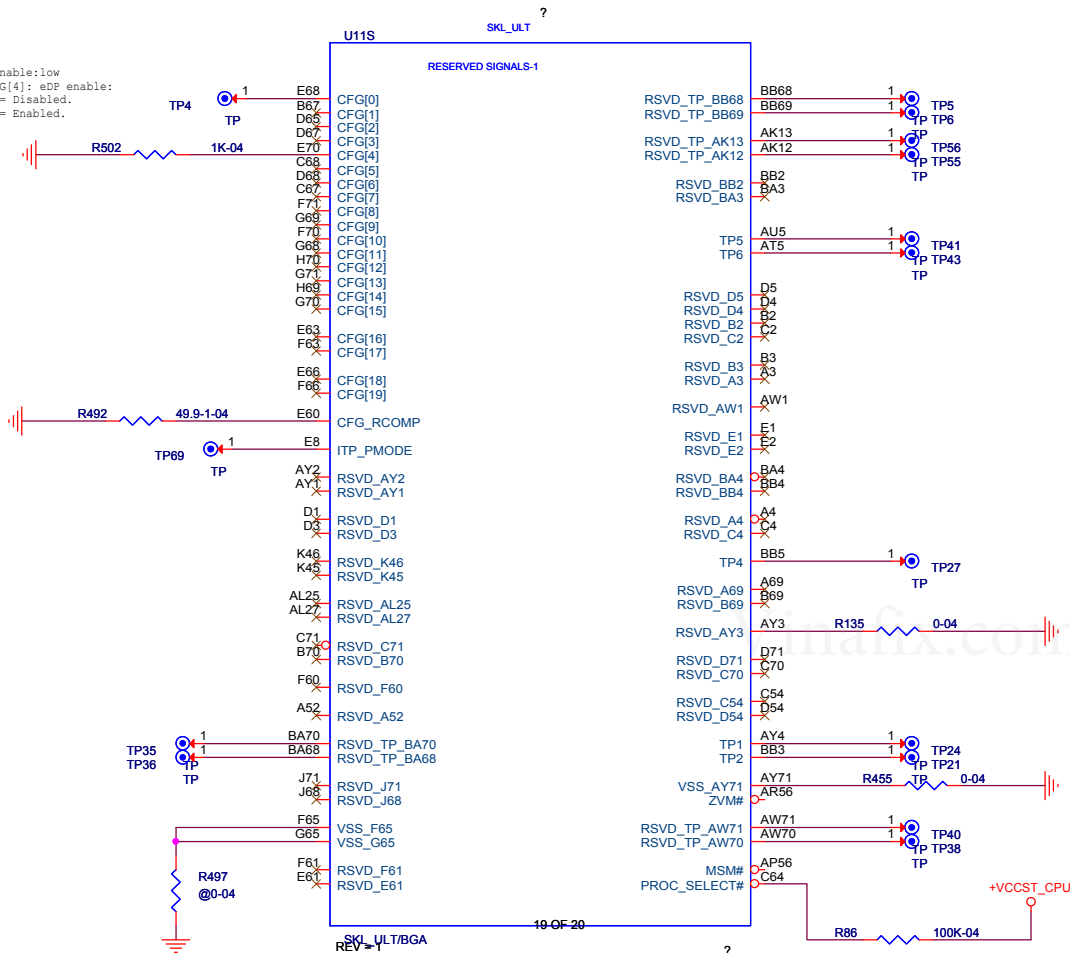
?

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Skylake-U		
Title		
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Broadwell USB/PCIE(6 of10)		
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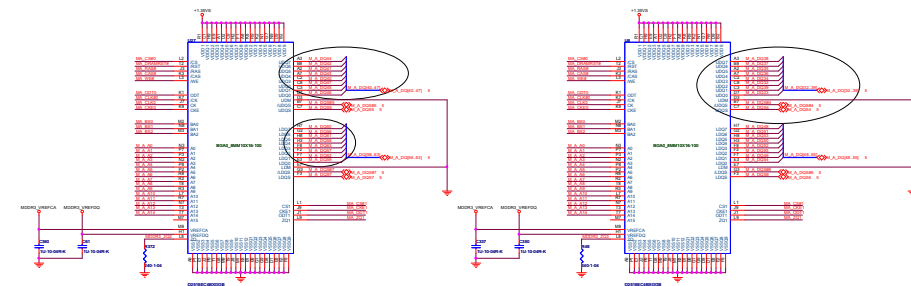
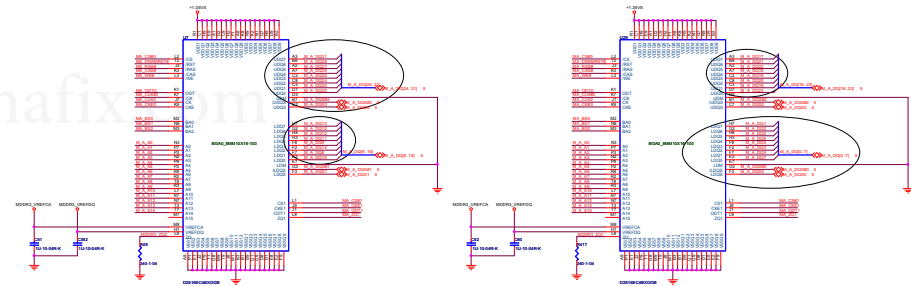
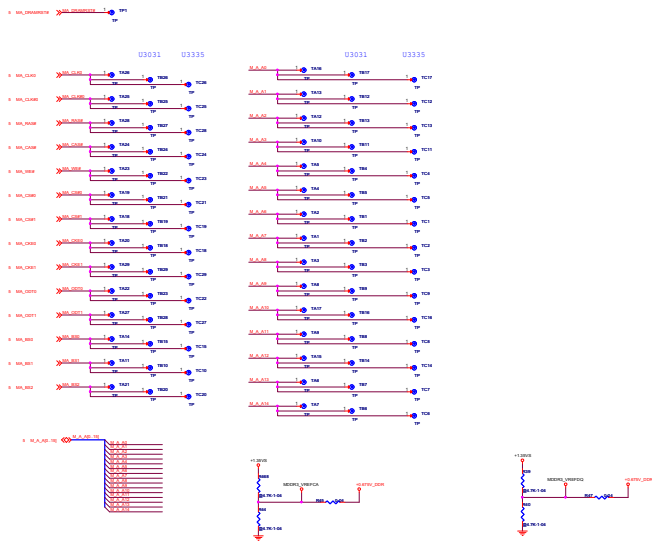
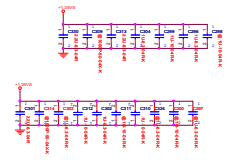
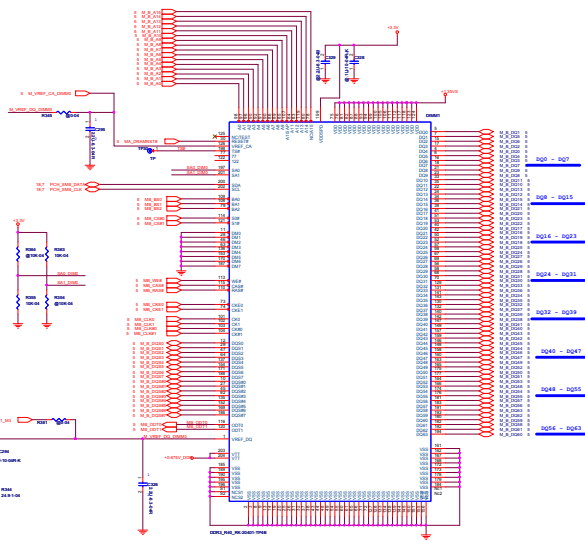


edp enable:low
CFG[4]: eDP enable:
1 = Disabled.
0 = Enabled.

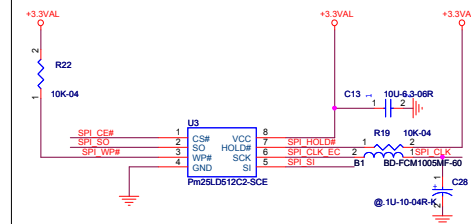


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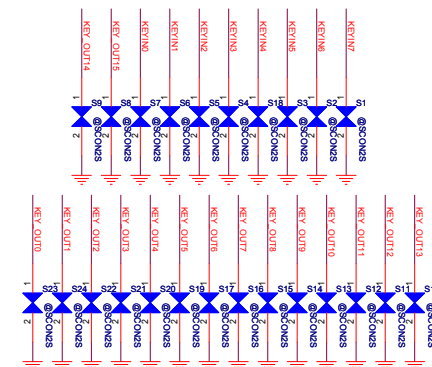
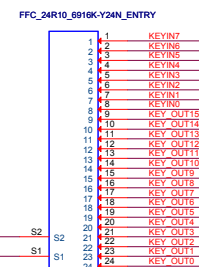
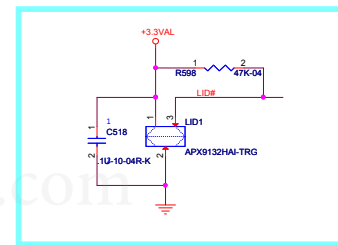
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Skylake-U			
Title			
Size	Document Number	Rev	
Custom	Broadwell RSVD(10 of 10)	B	
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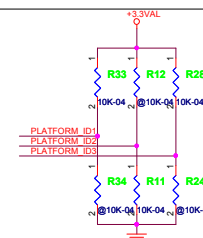
EC FLASH ROM(SPI)



EC SMBUS LEVEL SHIFT



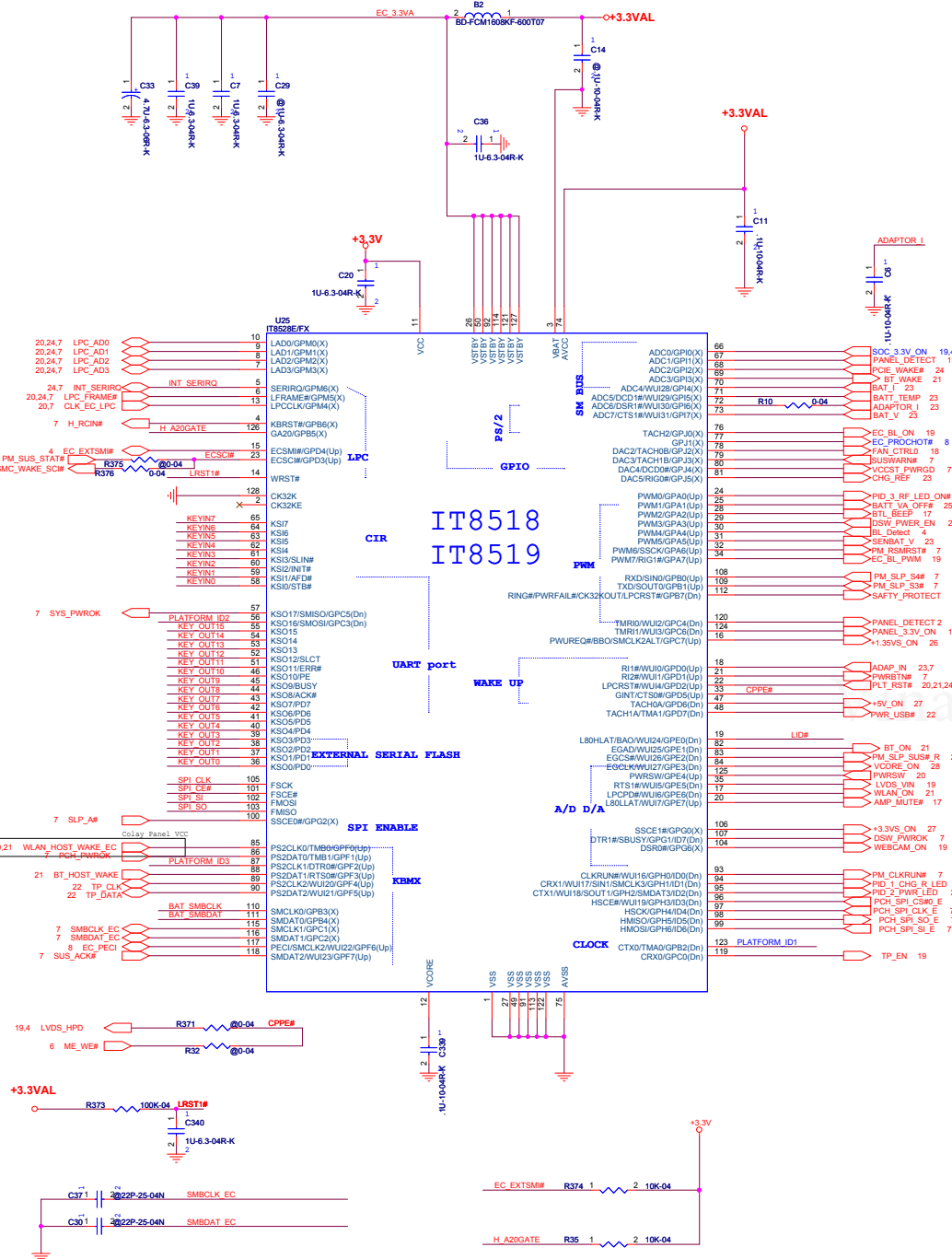
Platform ID



PLATFORM_ID1 HI= EDP LOW= LVDS
PLATFORM_ID2 HI= 65W LOW= 40W
PLATFORM_ID3 HI=LED DB LOW=LED on board

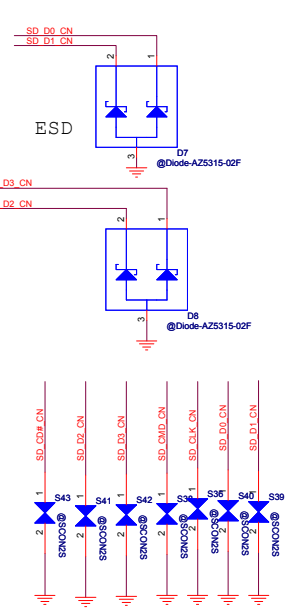
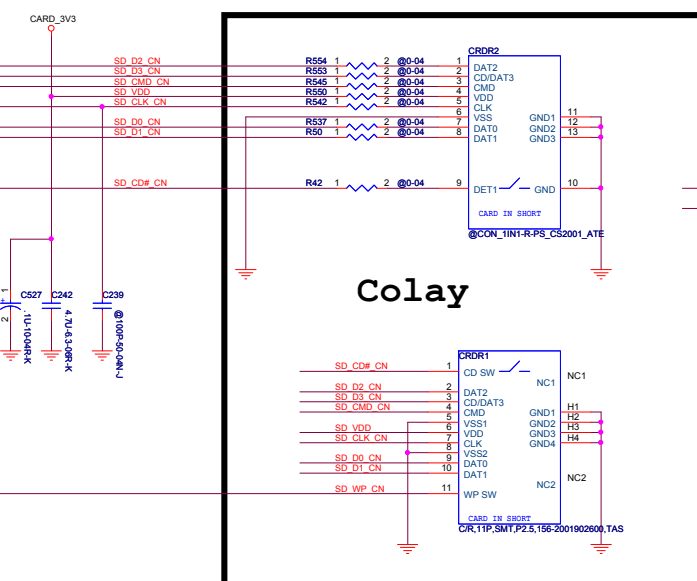
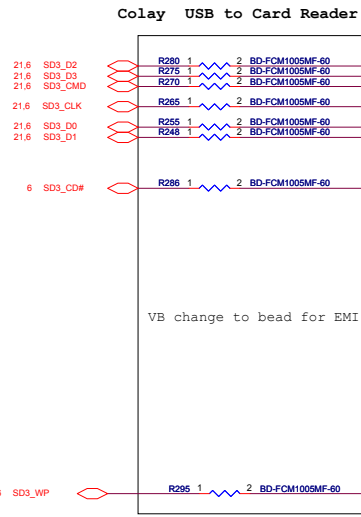
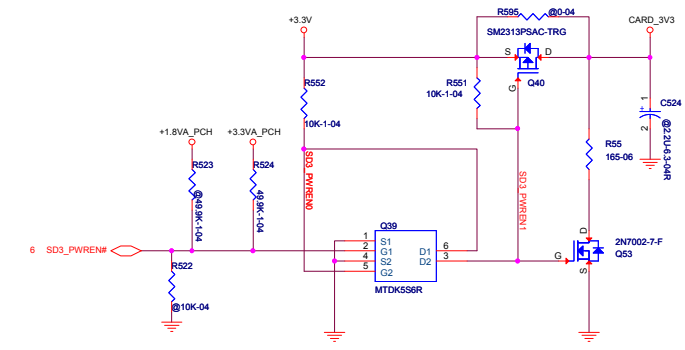
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Size Custom
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EC IT8528/BIOS/KB CONN/LID
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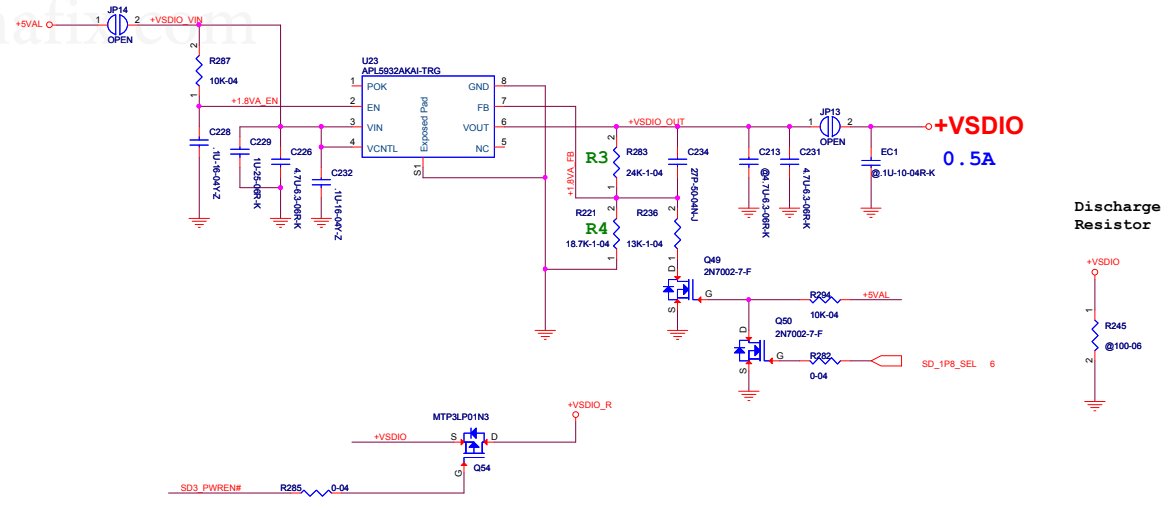
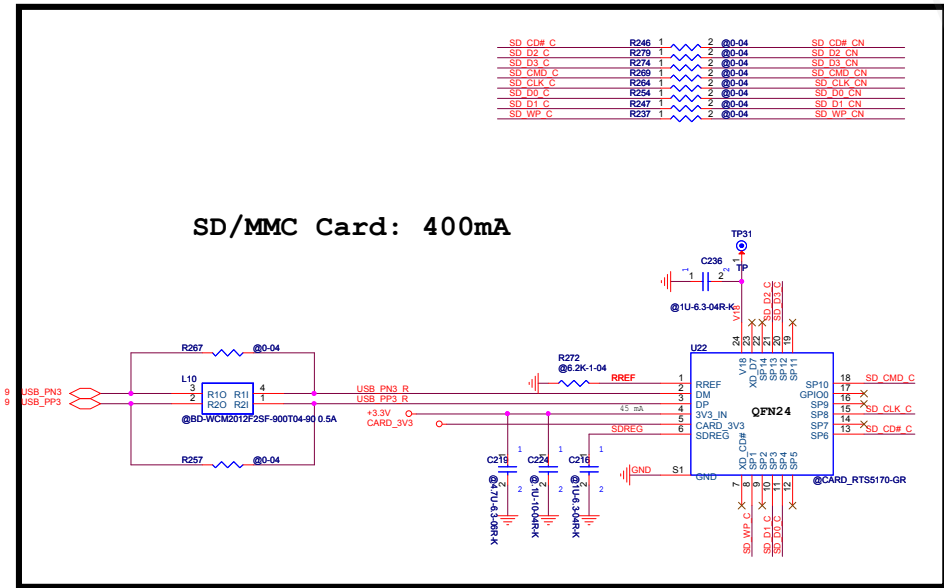


SMB0	only for Batt.
SMB1	PCH,G-sensor

SD POWER



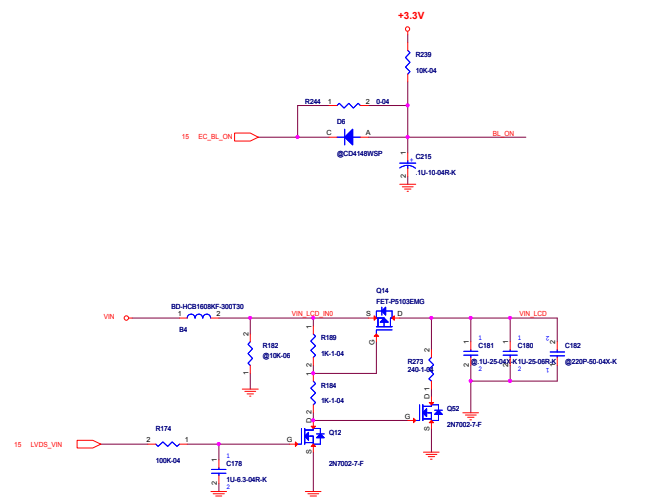
Support Ultra High Speed SD



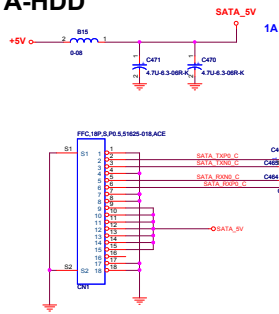
AMP VDD



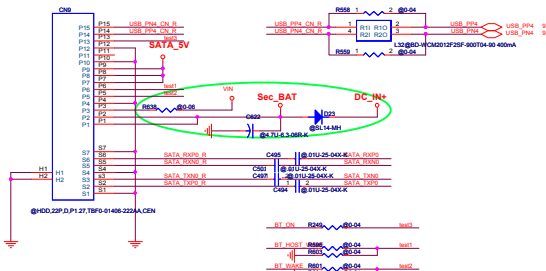
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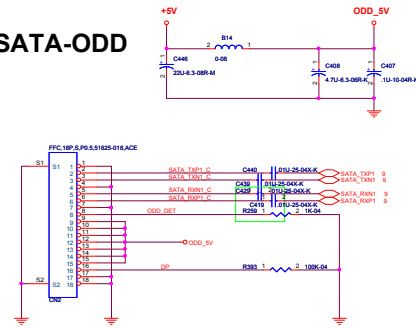
SATA-HDD



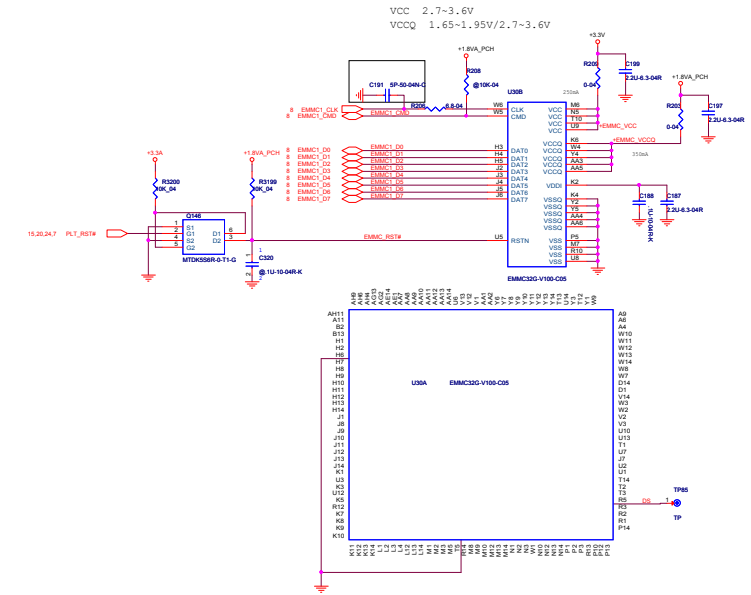
Colay



SATA-ODD

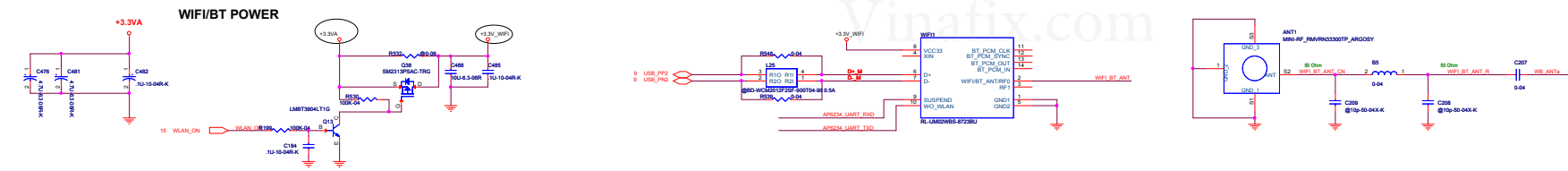


EMMC

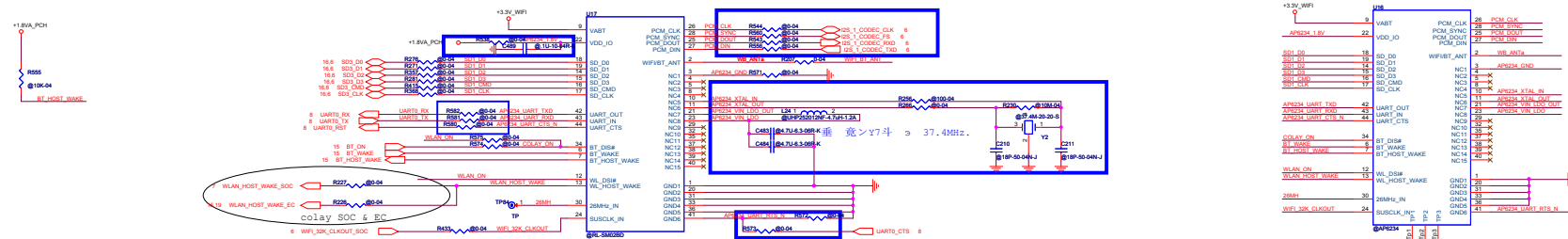


	W108.1	W107
W108.1	W108.1	W107
W107	W107	W107

WIFI & BT Combo

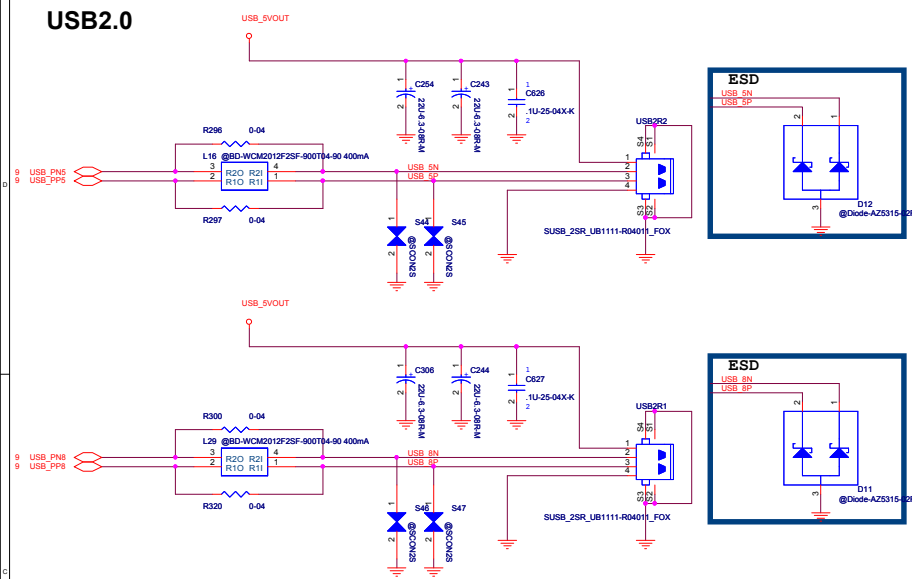


WIFI & BT Combo

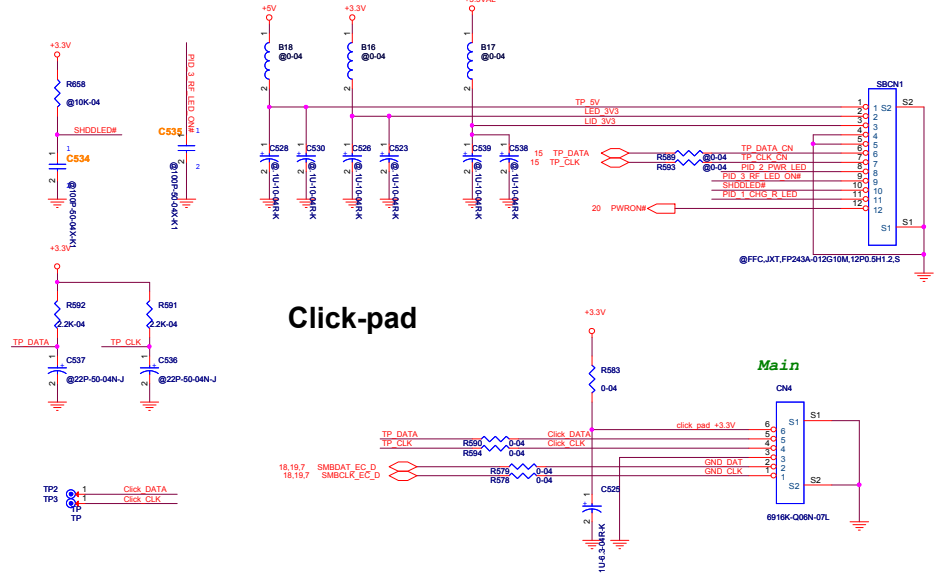


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Rev	Doc	Date	Rev
0	HDD/ODD/WIFI/EMMC	2018.09.10	0

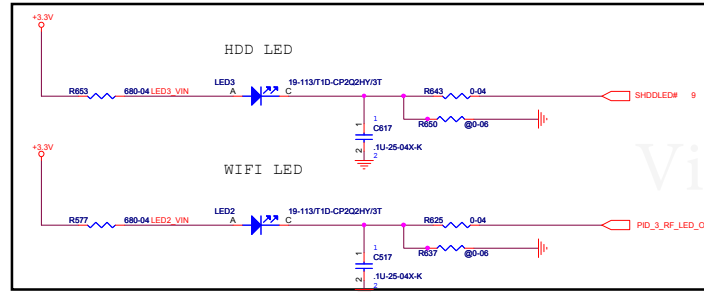
USB2.0



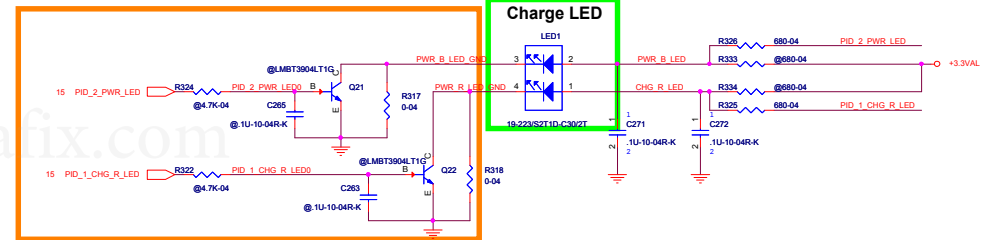
Touch Pad



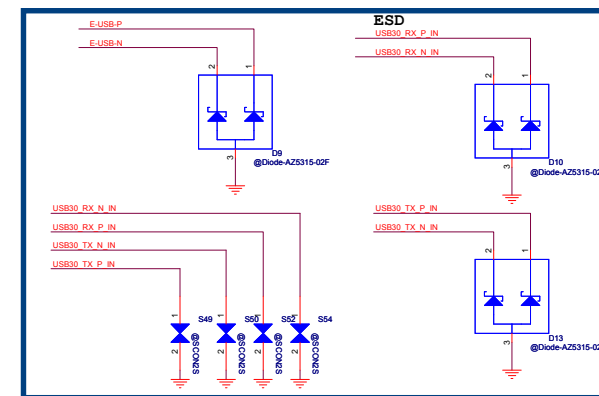
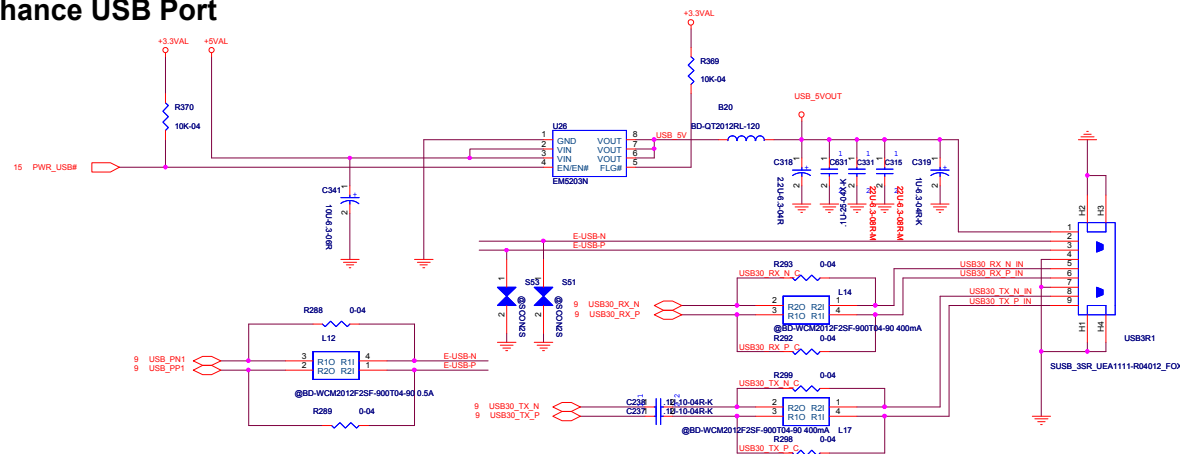
LED



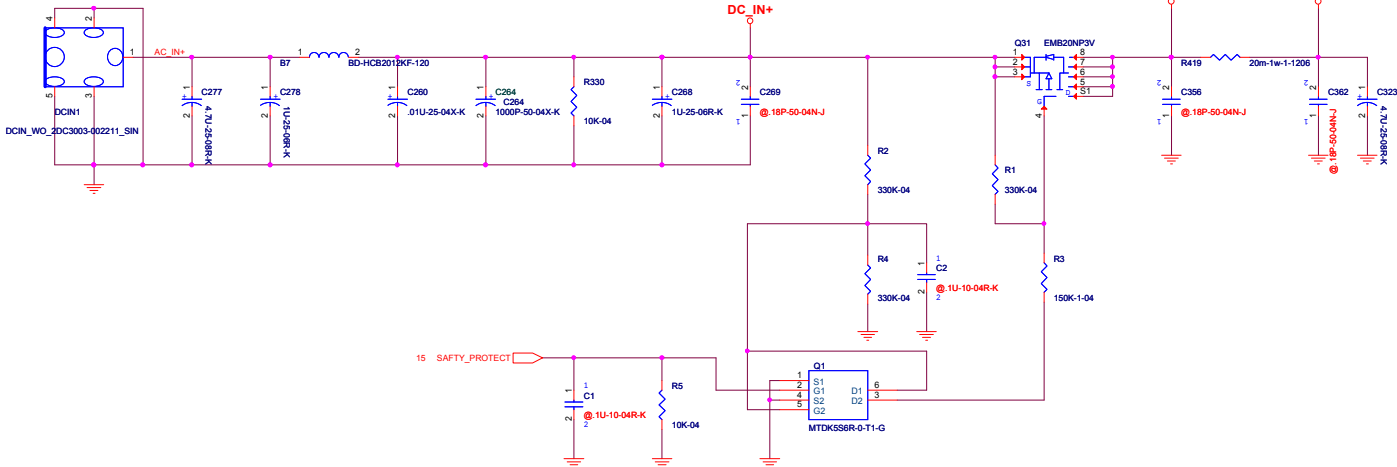
Charge LED



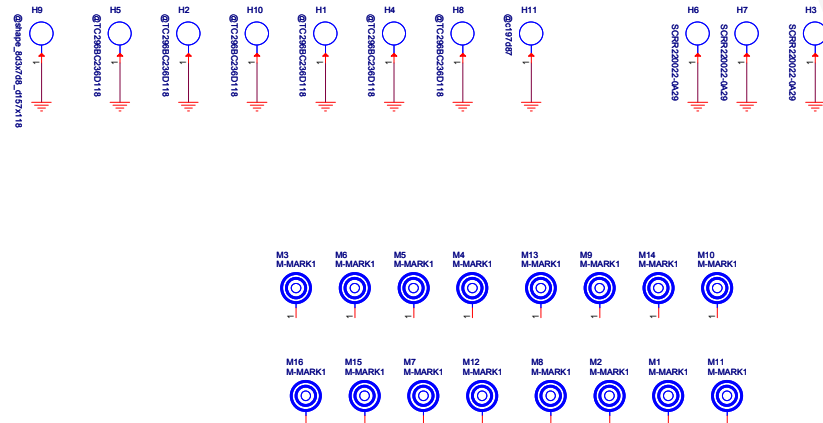
Enhance USB Port



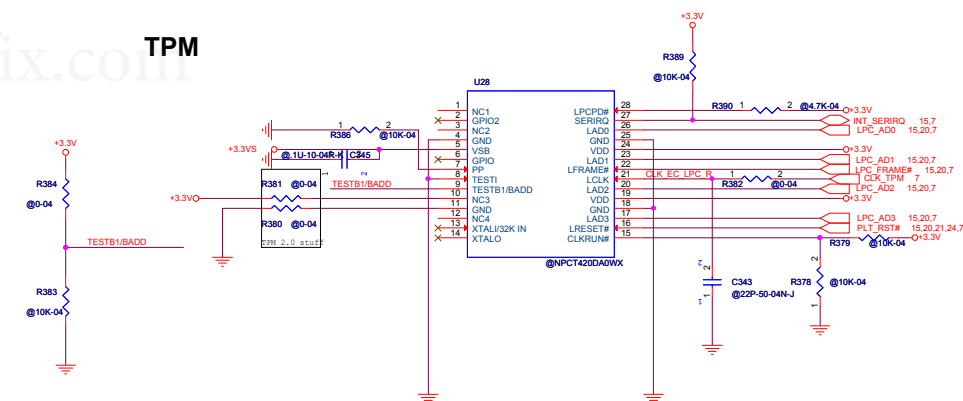
DC IN



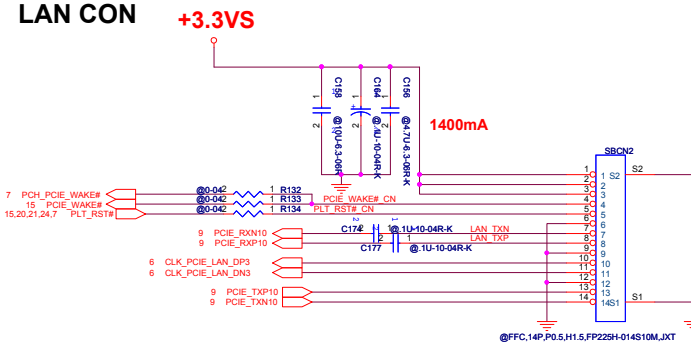
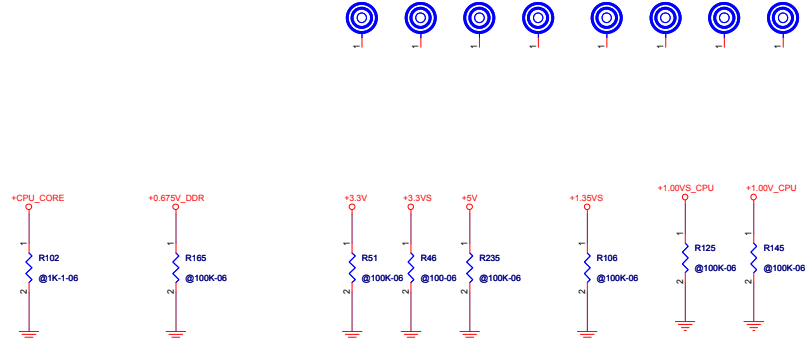
Discharge Resistor / Hole



TPM



LAN CON

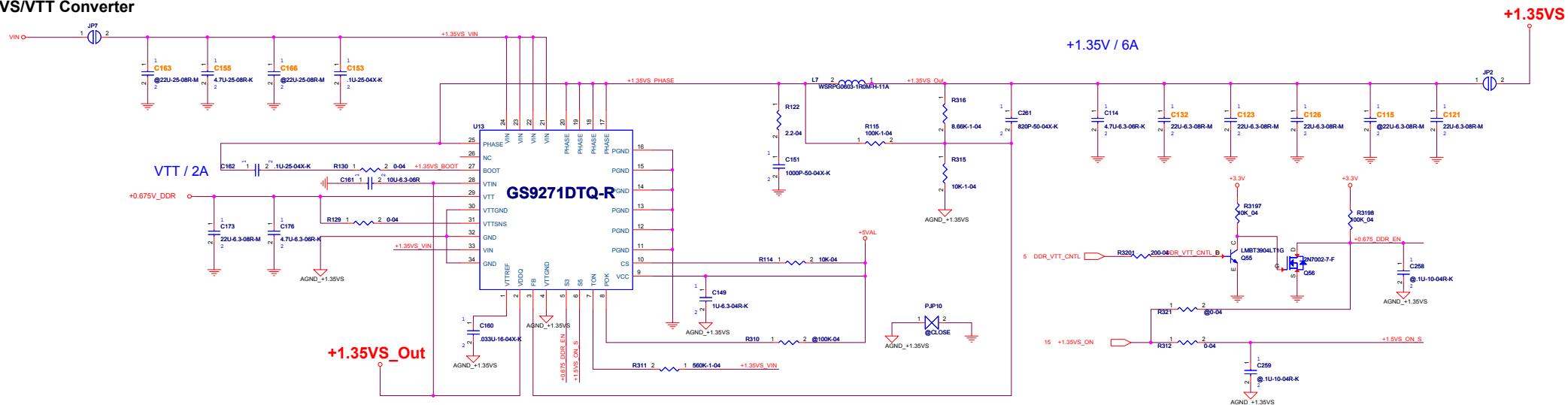


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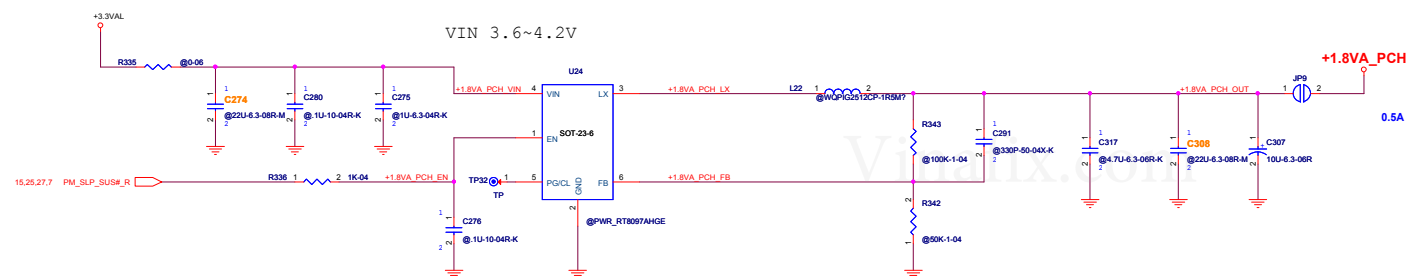
Title		Skylake-U	
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Custom	DC IN/TPM/LAN CON/D-Resis		
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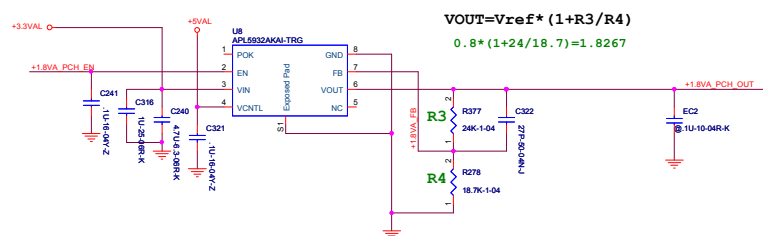
1.35VS/VTT Converter



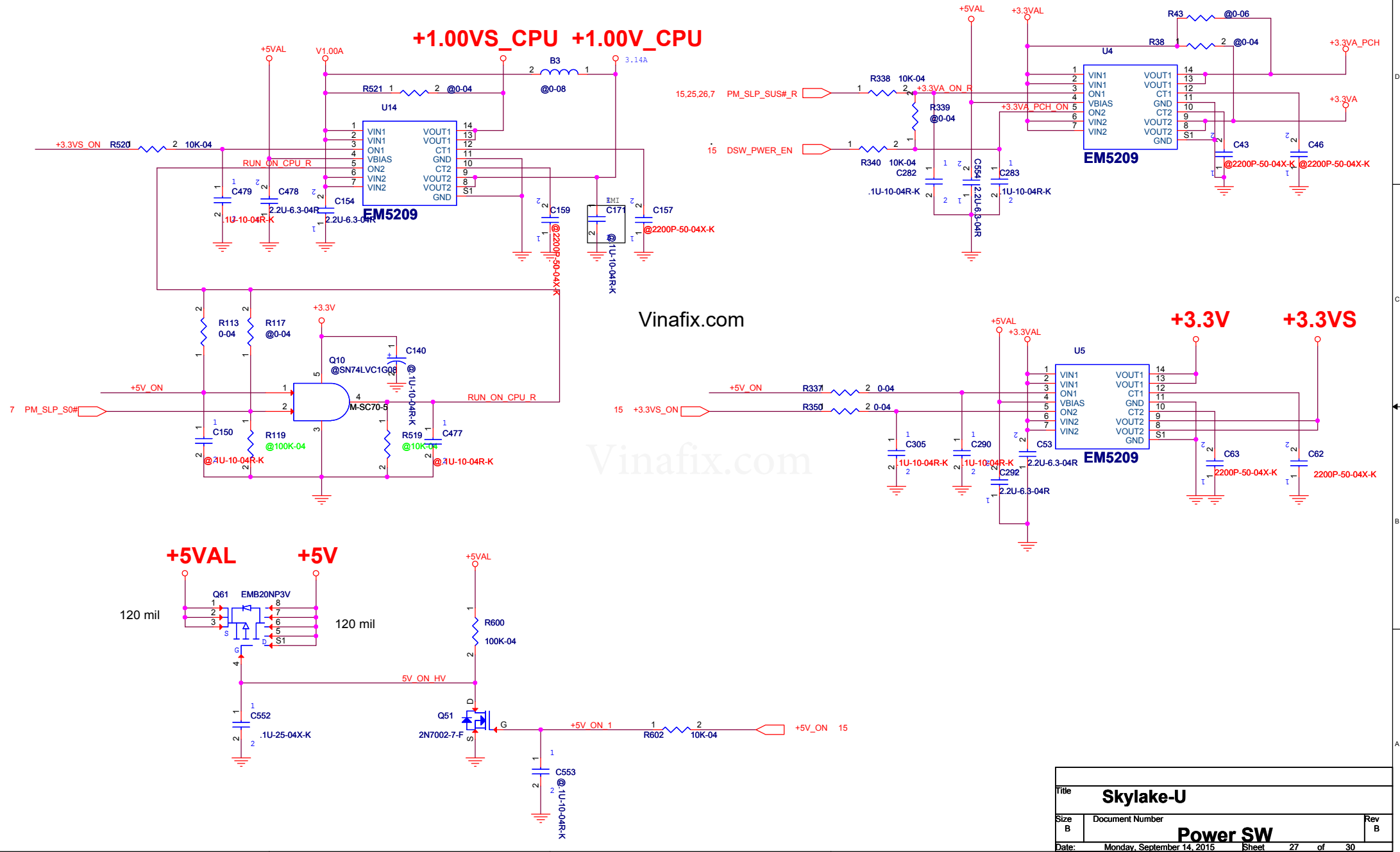
+1.8VA PCH	$V_{out}=0.6(1+R3/R4)$
-------------------	------------------------



+1.8VA_PCH



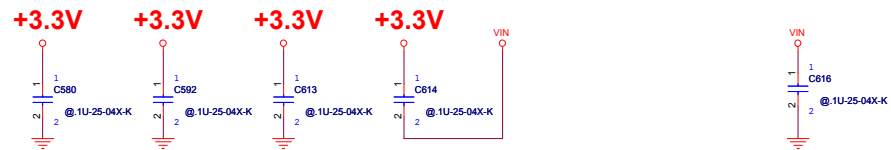
Power switch



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HIGH SPEED CAP



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1sub-bat CN5 PIN swap
2ODD CN2 PIN swap
3EC PIN118与PIN80对换
4SPK1改封装
5Q146改成Q55和Q56以及周围零件
6VSDIO/ U23改成LDO以及周围零件
7U8改成LDO以及周围零件
8V_CORE增加R694
9HDD CN1改成18pin ,pin define并放bottom面
10Q61改P MOS
11del R433
12U3 footprint改大
13del PS1, add B20
14Del SDIO WIFI U16, U17以及周边零件
15SD卡add R550, R295, R258,R55, Q53, Q54
16SODIMM swap MB_CLK0 & MB_CLK1#
17CCD SW增加Q57, Q58以及相关
18R670改接+5VAL
19AC present增加Q17
20RT3, RT4, RT5;, C116, C433改footprint
21EMMC_RST#增加Q146及相关
22R113改接+5V_ON



8/20. RF del WIFI A_GND
8/21 change R133,R125 from 22pf to 15pf
8/24 page 28 change as black ring.

VB Change item

9/14. 1. P25 R231 set on.
2. P26 R3201 change to 200 ohm.
3. P7 layout change R212,R453.
4. P21 EMMC connect A6,H5,J5 pin.
5. P27 set C282,C283 on, change R338,R340 to 10K.
6. P5 R436 change from 121 ohm to 200 ohm, R438 change from 100 ohm to 162 ohm.
7. P28 reserve POSCAP C589, C590, C558, C615.

9/15. 1. C213, C231 change to 0603; C178 change to 0402.
2. Add C323, C628, C629, C630 for APC
3. P16 change cardreader RES & DMIC RES to bead for EMI.
4. 9/18 add C520, C632.

Title		
Skylake-U		
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